

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT(S): Hassan Ihs et al                      GROUP ART UNIT: 2819  
APPLN. NO.: 10/534,119                      EXAMINER: Linh V. Nguyen  
FILED: May 5, 2005  
TITLE: CONVERTER CIRCUIT AND METHOD FOR COMPENSATION OF NON-  
IDEALITIES IN CONTINUOUS TIME SIGMA DELTA CONVERTERS

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Certificate of Submission

I hereby certify that this correspondence is being submitted to the  
U.S.P.T.O., Alexandria, VA.

- ☐ Addressed per C.F.R. § 1.1(a) and deposited with the United States Postal Service with sufficient postage as first class mail.  
☐ Facsimile transmitted in accordance with C.F.R. § 1.6(d).  
☒ Submitted electronically via EFS in accordance with "Legal Framework for EFS Web".

8-16-06  
Date of Submission

Signature

Printed Name of Person Signing Certificate

**NOTICE OF APPEAL FROM THE EXAMINER TO THE  
BOARD OF PATENT APPEALS AND INTERFERENCES**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

SIR:

In accordance with 37 CFR 1.191, applicant hereby appeals to the Board of Patent Appeals and Interferences from the last decision of the examiner. The Commissioner is hereby authorized to charge any fees which may be required, or credit any overpayment to Deposit Account No. 503079, Freescale Semiconductor, Inc. This Notice is submitted in duplicate.

Respectfully submitted,  
Hassan Ihs et al



FREESCALE SEMICONDUCTOR, INC.  
Customer Number: 23125

David G. Dolezal  
Attorney of Record  
Reg. No. 41,711  
Tel.: (512) 996-6839  
Fax: (512) 996-6853